

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5      Applicant(s): Theeuwen et al.                          Group Art Unit: 2815  
Serial No. 10/569,171                                  Confirmation No. 7877  
Filed: February 22, 2006                                  Examiner: Ho, Anthony  
For:    ELECTRIC DEVICE COMPRISING AN LDMOS TRANSISTOR

10     Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

15                          RESPONSE TO RESTRICTION/ELECTION

Sir/Madam:

20     In the Office Action dated January 3, 2008, the Examiner has required a  
restriction to one of the following inventions:

- I:       Claims 1-4, 6, 7 and 10, drawn to an electronic device comprising a  
transistor having a stepped shield structure in a drain extension area;  
II:      Claims 5, 8 and 9, drawn to an electronic device comprising a  
transistor having a higher doped drain extension portion adjoining a  
channel; and  
III:     Claim 11, drawn to a method of manufacturing an electronic device.

25     In response, Applicants elect without traverse Invention I for prosecution.  
Claims 1-4, 6, 7 and 10 encompass the elected Invention I.

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Applicants respectfully suggest that the pending claims in the patent application are distinct over the prior art and that the application is in condition for allowance. Accordingly, a notice of allowance is earnestly solicited.

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Respectfully submitted,  
Theeuwen et al.

Date: February 1, 2008

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